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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/751,788	12/29/2000	Yat H. Chan	1662-32800 (P00-3347)	8778
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HEWLETT PACKARD COMPANY  
P O BOX 272400, 3404 E. HARMONY ROAD  
INTELLECTUAL PROPERTY ADMINISTRATION  
FORT COLLINS, CO 80527-2400

EXAMINER

DU, THUAN N

ART UNIT

PAPER NUMBER

2116

DATE MAILED: 09/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 09/751,788	<b>Applicant(s)</b> CHAN ET AL.	
	<b>Examiner</b> Thuan N. Du	<b>Art Unit</b> 2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 21 June 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-16 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

### **DETAILED ACTION**

1. It is hereby acknowledged that the following papers have been received and placed of record in the file: Amendment (dated 6/21/05).
2. Claims 1-16 are presented for examination.
3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
4. The indicated allowability of claims 1-8 and 14-16 is withdrawn in view of the newly discovered reference(s) to applicant's admission of prior art [AAPA] in view of Lee (U.S. Patent No. 6,222,799). Rejections based on the newly cited reference(s) follow.

#### ***Claim Rejections - 35 USC § 103***

5. Claims 1-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over applicant's admission of prior art [AAPA] in view of Lee (U.S. Patent No. 6,222,799).
6. Regarding claim 1, AAPA teaches a computer system comprising:  
  
initialization memory containing initialization code [application's specification, p. 2, lines 4-5];  
  
a processor coupled to said initialization memory for executing said initialization code [application's specification, p. 2, line 12]; and  
  
a memory (processor's registers) coupled to said processor and used to store variables used by the processor while executing said initialization code [application's specification, p. 2, lines 8-12].

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AAPA does not explicitly teach that the memory used for storing variable is a static random access memory (SDRAM).

Lee teaches a system comprising:

initialization memory (ROM 922) containing basic codes used by a processor (921) [Fig. 9; col. 11, lines 12-13];

a processor (921) coupled to said initialization memory [Fig. 9] for executing said basic codes [col. 11, lines 12-13]; and

a static random access memory (SRAM 924) coupled to said processor [Fig. 9] and used to store any basic information (including information needed for system boot up) [col. 11, lines 17-18, 26-30].

wherein said SRAM continues to receive power even if said system is otherwise powered off [col. 11, lines 40-41], said SRAM thereby being available for use by the processor without needing to subsequently be initialized when the processor executes said codes [col. 11, lines 26-31, 35-39].

Therefore, it would have been obvious to modify the teachings of AAPA by replacing the processor's registers with the SRAM taught by Lee. The modification would increase the boot time of the system [Lee, col. 11, lines 26-31].

7. Regarding claim 2, one of ordinary skill in the art would have recognized that in order to use SRAM for executing codes stored in initialization memory, the SRAM is obviously assigned address overlaying a portion of the address space assigned to the initialization memory, otherwise the SRAM could not be cooperated with the initialization memory to execute the codes stored in initialization memory.

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8. Regarding claim 3, Lee teaches that the ROM stores codes needed by the processor [col. 11, lines 12-13] and the SRAM stores information needed for the processor upon system boot up. Therefore, it would have been obvious to one of ordinary skill in the art to recognize that Lee would include a logic used for selecting either ROM or SRAM needed by the processor.
9. Regarding claim 4, AAPA teaches that the system further including a dynamic random access memory (DRAM) coupled to the processor, wherein said initialization code is for initializing said DRAM [application's specification, p. 2, lines 4-5].
10. Regarding claim 5, AAPA teaches that the processor uses primarily only said DRAM when executing application code [application's specification, p. 1, lines 22-25].
11. Regarding claims 6-16, they do not teach or further define over the limitations recited in the rejected claims 1-5 above. Therefore, claims 6-16 are also rejected as being unpatentable over AAPA in view of Lee for the same reasons set forth in the rejected claims 1-5 above.

### ***Conclusion***

12. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan N. Du whose telephone number is (571) 272-3673. The examiner can normally be reached on Monday-Friday: 9:30 am - 6:00 pm, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on (571) 272-3670.

Central TC telephone number is (571) 272-2100.

The fax number for the organization is (571) 273-8300.

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13. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll free).

A handwritten signature in black ink, appearing to read 'Thuan N. Du', with a stylized flourish at the end.

Thuan N. Du  
September 3, 2005